NANO-001/09U eby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the Assistant Commissioner for Patents, Washington, D.Q. 20231, on April 8, 1996 Date: 4-8-9 IN THE UNITED STATES PATENT AND TRADEMARK OFFICE EQUIPED AFR 2 4 1996 In re application of Examiner: D. Eng GROUP 2300 Charles H. Moore et al. Art Unit: 2315 Serial No. 08/484,935 Filed: June 7, 1995 **AMENDMENT** For: HIGH PERFORMANCE, LOW COST MICROPROCESSOR Palo Alto, CA 94306 Assistant Commissioner for Patents Washington, D.C. 20231 Sir:

This Amendment is being submitted in response to the first Office Action in the above-identified patent application.

IN THE SPECIFICATION

At page 1, line 1, please change the title from "HIGH PERFORMANCE, LOW COST MICROPROCESSOR" to --HIGH PERFORMANCE MICROPROCESSOR USING INSTRUCTION GROUPS--.

0/96 0848497 000CH

300 SL 04/22/96 08484935 1 215 55.00 CK N07652021

21091016

1.